


09/28/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER _____

U.S. **UTILITY** Patent Application

	O.I.P.E. SCANNED <u>TM³</u> Q.A. <u>CTH</u>	PATENT DATE
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APPLICATION NO: 09/671065	CONT/PRIOR	CLASS 71	SUBCLASS 129	ART UNIT 285 2101	EXAMINER VO
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APPLICANTS

[illegible]

TITLE

Mechanism for allowing PCI-PCI bridges to cache data without any coherency side effects

PTO-204

[illegible]

PTO 204
12/89

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED _____			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE <table border="1"> <tr> <td>Amount Due</td> <td>Date Paid</td> </tr> </table>		Amount Due	Date Paid
Amount Due	Date Paid						
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER			
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